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(12) United States Patent
Lee**(10) Patent No.: US 6,259,732 B1**
(45) Date of Patent: Jul. 10, 2001**(54) METHOD AND APPARATUS FOR
ENCODING INTERLACED MACROBLOCK
TEXTURE INFORMATION****(75) Inventor: Sang-Hoon Lee, Seoul (KR)****(73) Assignee: Daewoo Electronics Co., Ltd., Seoul
(KR)****(*) Notice:** Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 0 days.

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(21) Appl. No.: 09/088,375**(22) Filed: Jun. 2, 1998****(30) Foreign Application Priority Data**

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(51) Int. Cl.⁷ H04N 7/12**(52) U.S. Cl. 375/240; 375/240.24****(58) Field of Search** 382/239, 243,
382/236, 154, 250; 348/416, 420; 375/240,
240.24**(56) References Cited****U.S. PATENT DOCUMENTS**

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Assistant Examiner—Allen Wong
(74) Attorney, Agent, or Firm—Pennie & Edmonds LLP**(57) ABSTRACT**

A method for padding interlaced texture information on a reference VOP to perform a motion estimation detects whether said each texture macroblock of the reference VOP is a boundary block or not. After the undefined texture pixels of the boundary block are extrapolated from the defined texture pixels thereof by using a horizontal repetitive padding, a transparent row padding and a transparent field padding sequentially, an undefined adjacent block is expanded based on the extrapolated boundary block.

14 Claims, 6 Drawing Sheets